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News center

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March 30, 2020 | Tags: COVID-19, COVID-19 in IEEE Xplore, COVID-19 research



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View Articles



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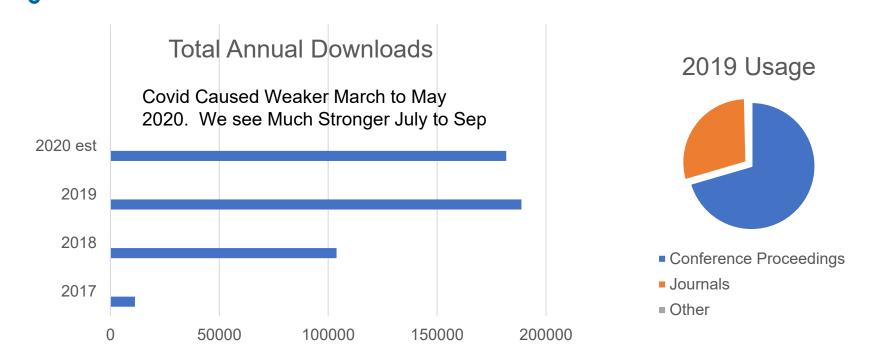
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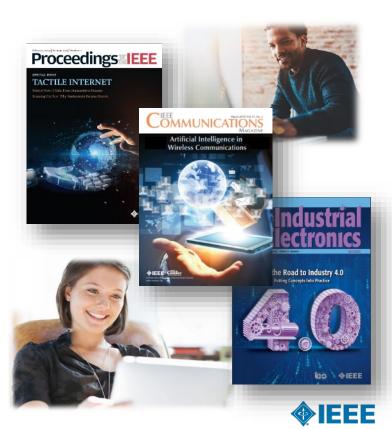


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1.28 million

3.6 million

10,500

500

47,000

Approximate counts from IEEE *Xplore* March 2020

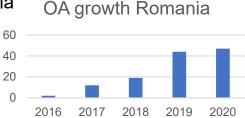


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- In 2020, there are 23,432 articles by authors from Romania
- In the period from 2010–2020 more than <u>3 times more</u> articles were published by authors from Romania
- IEEE Xplore includes 132 Open Access articles by authors from Romania

89 from them in







**IEEE Publications Strategy and Goals** 

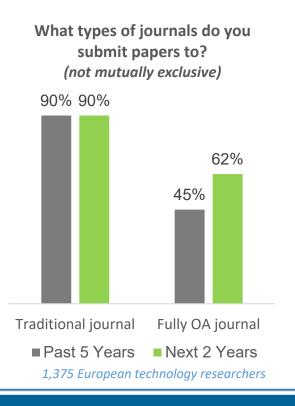
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Source: IEEE/IET Study on Researcher Behavior: Current and Future Trends, 2017"



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	New IEL Subscribed Documents	Growth in IEL Subscribed Documents	Total New IEEE Open Access Dcouments	OA Portion of Total New IEEE Documents
2019*	277,222	<b>1</b> 4.1%	20,113	6.7%
2018	266,233	<b>↑</b> 3.3%	10,473	3.8%
2017	257,786	<b>↑</b> 2.7%	6,655	2.5%
2016	250,934	<b>↑</b> 3.8%	3,024	1.2%
2015	241,832	<b>↑</b> 5.8%	2,238	0.9%
2014	228,519	<b>↑</b> 3.0%	2,722	1.2%

#### Included in IEL

#### Not included in IEL



<sup>\*</sup> IEL content defined as IEEE & IET journals,, conference and standards documents exclusively available to subscribers. Source: IEEE Xplore queries April 2020. Includes sponsored conference content in queue for posting.

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MEN!

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## **New IEEE Journals Coming in 2020 & 2021**

These new journal titles\* will soon be available and accessible via subscription:

- IEEE Journal of Emerging and Selected Topics in Industrial Electronics
- IEEE Journal on Selected Areas in Information Theory
- IEEE Transactions on Technology and Society
- IEEE Transactions on Artificial Intelligence
- IEEE BITS the Information Theory Magazine





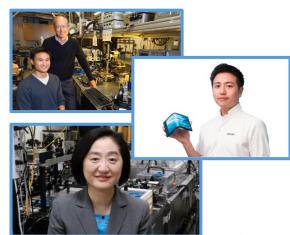




## IEEE's Shift to Virtual Conferences Keeps the Research Community Connected and Publishing

- IEEE has adapted to support the technical community and continues to drive innovation forward during this challenging time. IEEE is continuing to help authors disseminate their research and publish new papers.
- The majority of IEEE conferences continue on in a new virtual format and many are seeing record attendance levels and attracting papers from a global audience.
- Example:
  - The CLEO conference was held in May 2020
  - Nearly <u>20,000</u> registrants from 75 countries
  - Over <u>1,800</u> scientific papers a 20% increase!
  - All the papers presented at this conference are now available in the IEEE Xplore digital library







# **IEEE Conferences Continue to Address Growing Areas of Research in New and Emerging Technologies**

IEEE conferences continue to address growing areas of research that transform our lives. Below are some examples of conferences published in 2020 covering these innovative technologies:

- IEEE Int'l Conf on Blockchain and Cryptocurrency (ICBC)
- 2020 Conf on Data Science and Machine Learning Applications
- World Symposium on Artificial Intelligence (WSAI)
- Int'l Conf on Artificial Intelligence and Big Data (ICAIBD)
- Int'l Conf on Connected and Autonomous Driving
- 2020 Global Internet of Things Summit (GIoTS)
- IEEE Int'l Conf on Cyber Security and Cloud Computing
- IEEE Int'l Conf on Power Electronics, Smart Grid, and Renewable Energy
- Int'l Conf on Renewable Energies for Developing Countries
- Conf on Innovation in Clouds, Internet and Networks, and Workshops (ICIN)
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- 2020 3rd Int'l Conf on Engineering: Machine Learning and Internet of Things





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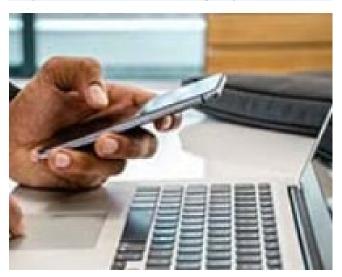


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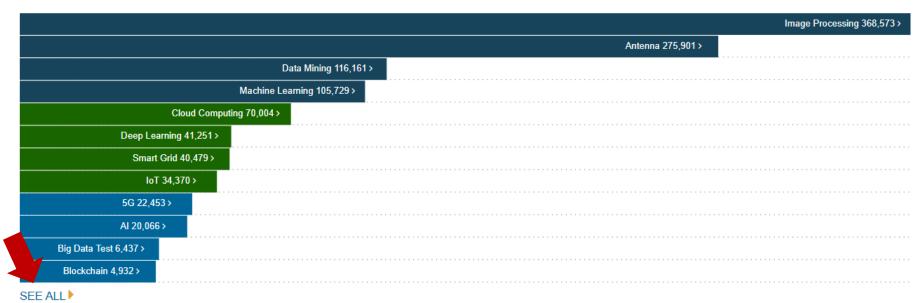
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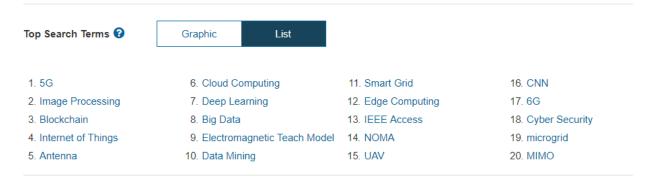






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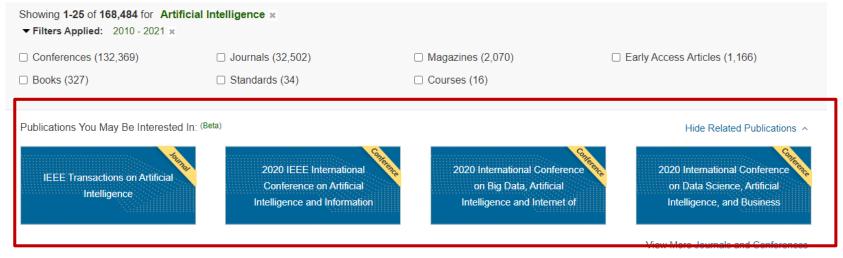
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**Sept 2020** 

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The Perspective of Dimensional Perpetuity for Artificial Intelligence: A Model on Socio-Legal and Political Evolution as a Challenge to Entrepreneurial Ethics

Abhiyardhan

2020 2nd World Symposium on Artificial Intelligence (WSAI)

Year: 2020 | Conference Paper | Publisher: IEEE

((html))







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#### Standards Dictionary Terms ?

- fault isolation
- interoperability
- range
- result
- ambiguity group
- artificial intelligence (AI)
- assertion



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Document Sections

II. Situational Information

in Social Media

I Introduction

 Quickly and easi Cite This **Characterizing the Propagation** During COVID-19 Epidemic: A C Plain Text BibTeX RIS Refworks Publisher: ∠ PDI Cite This □ Citation & Abstract Lifang Li; Qingpeng Zhang (b); Xiao War 9 Author(s) L. Li et al., "Characterizing the Propagation of Situational Information in Social Media During COVID-8467 Paper 19 Epidemic: A Case Study on Weibo," in IEEE Transactions on Computational Social Systems, vol. 7. Citations Text Views no. 2, pp. 556-562, April 2020, doi: 10.1109/TCSS.2020.2980007. **1** Free Copy Abstract Abstract:

During the ongoing outbreak of coronavirus disease (COVID-19), people use social media to acquire and

important to identify such situational information and to understand how it is being propagated on social media, so that appropriate information publishing strategies can be informed for the COVID-19 epidemic.

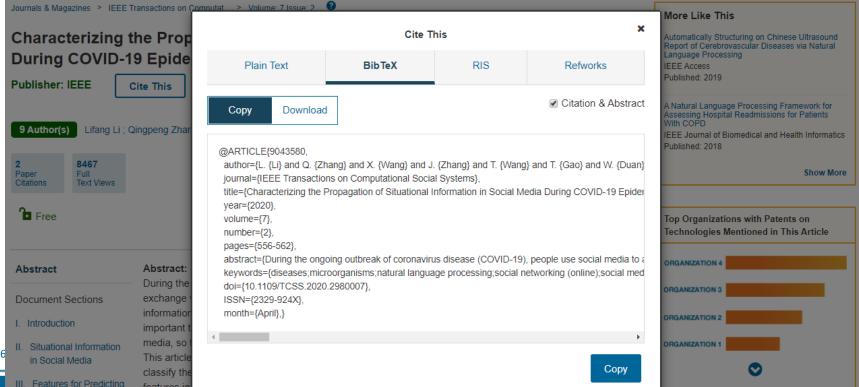
classify the COVID 10 related information into seven types of situational information. We found enecific

This article sought to fill this gap by harnessing Weibo data and natural language processing techniques to

exchange various types of information at a historic and unprecedented scale. Only the situational information are valuable for the public and authorities to response to the epidemic. Therefore, it is

**IEEE** 

## **Streamlined Method for Downloading Citations**



features in

# New Supplemental item –Video for conference papers

#### Select All on Page Sort By:Newest First ▼ Show All Results SR Latch: The Wrong Introduction to Digital Memory Abdulhadi Shoufan Subscribed Content ? 2020 IEEE International Symposium on Circuits and Systems (ISCAS) Open Access Only Year: 2020 | Conference Paper | Publisher: IEEE (( html)) (128 Kb) **(C)** Video Abstract Supplemental Items A High Efficiency Frequency-Modulated 1/2X Switched Capacitor DC-DC Converter with Wide Load Range Media (33,799) Mengyu Li; Menglian Zhao; Sheng Liu; Haonan Wu; Zhao Yang; Xiaobo Wu Video (897) 2020 IEEE International Symposium on Circuits and Systems (ISCAS) Datasets (365) Year: 2020 | Conference Paper | Publisher: IEEE Code (306) (( html)) Video Abstract (1106 Kb) **Apply**



SR Latch: The Wrong Introduction to Digital Memory

SR Latch: The Wrong Introduction to Digital Memory

Transcript

[00:00] ABDULHADI SHOUFAN Hello, everyone. This is Abdulhadi Shoufan. I am the head [INAUDIBLE] and director engineering and computer science department at Khalifa University. I would love to talk to you about a teaching experience in teaching digital logic design, specifically when we move from the combinatorial logic to the sequential logic and we have to explain the digital memory [INAUDIBLE] students as a basic component of sequential logic.

[00:30] ABDULHADI SHOUFAN The motivation for my thoughts and, later, for writing the paper is actually the discomfort that we have when we explain this topic to a student. Not all topics are equally difficult in digital logic design. And, usually, we have different ways to deal with complex topics. Either we skip them completely, or we abstract from the details and tell the students that this topic is too complex

[01:08] ABDULHADI SHOUFAN So we abstract from the technicality of the topic, or we deal with it and try our luck. So, when it comes to [? notches ?] and flip-flops, there is no way to go around it. We have to deal with it because these components build the basics or the foundation for registers, shift registers, [? countdowns, ?] finite state machines, and so on.

[01:40] ABDULHADI SHOUFAN So there is no way to go around it. The problem with this is that textbooks are not very helpful when it comes to explaining the flip-flops. And we can see that textbooks are actually. they skip the basic concept, or they abstract from it and start dealing with implementation of

[02:10] ABDULHADI SHOUFAN And this is a major, major issue actually. So, if you want to explain the 1bit memory concept, we should go to the basics and try to define what a memory is or 1-bit memory is. We have a bit we want to store. And we make a storage for it. Of course, the storage should be controlled and that we would like to store at specific time points and pay attention to the bit line.

[02:52] ABDULHADI SHOUFAN And that's why we need a control signal. I am calling it attention signal, here. And, when the bit is stored, we would like to have access to it, so we need an output line on the storage. And to simplify the explanation, I would like to now give these signals letters, so D, E, and Q. So we should be consistent with combinatorial [INAUDIBLE] of the digital logic design and try to design 1-bit memory starting from a [INAUDIBLE] table.

[03:38] ABDULHADI SHOUFAN And this the first table for 1-bit memory. We have that control signal E when it is 1. We store what is on D into Q. And, when the control signal E, that attention signal is zero, we just keep the output value as it is. And, if you look at this Q signal- now I'm extracting from the timingyou can write this equation or boolean function. And, as you can see that the Q is nothing but the output of a multiplexer, where the output goes back to the input to the zero input of the multiplexer.

[04:22] ABDULHADI SHOUFAN And this is actually the very basic concept of 1-bit memory. But, unfortunately, I reviewed probably 10 or more textbooks, all textbooks that I reviewed don't have this simple explanation. Or they do-- instead, they try many circuits until they get to a good [INAUDIBLE]..

[04:52] ABDULHADI SHOUFAN And they some textbooks show this two cross-coupled inverter as a bistable element for storage. But they criticize it because it has no inputs or outputs. Then, they throw the

### **Motivation: Explanation Discomfort**

for writing the paper is actually the discomfort





Khalifa University, Abu Dhabi, UAE



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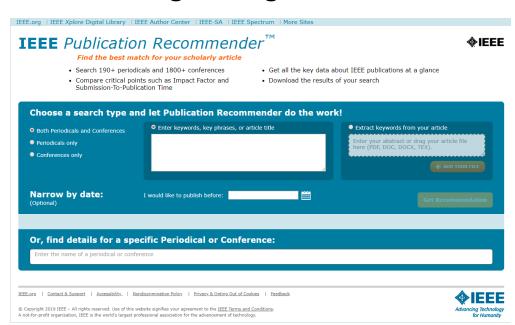
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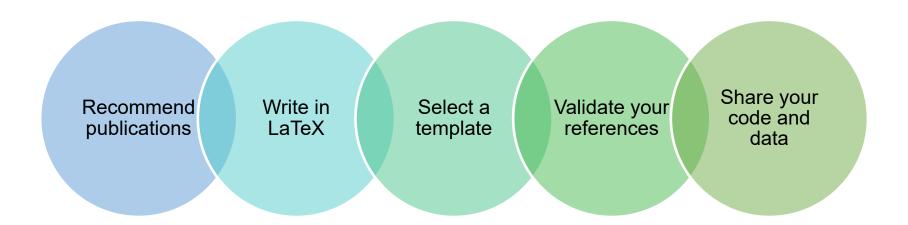
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